

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Segaram, Para K.
Assignee: Rambus, Inc.
Title: "High Speed Communication System With A Feedback Synchronization Loop"
Serial No.: Unknown Filed: January 16, 2001
Examiner: Tel:
Docket No.: RA-162-1P Art Unit:

1c978 U.S. PTO
09/761211
01/16/01

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Box Patent Application
Assistant Commissioner For Patents
Washington, D.C. 20231

January 16, 2001

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicant brings the 2 documents listed on the enclosed form PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant application;
2. a representation that a search has been made; or
3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

Copies of the listed documents are not provided because they were cited in a prior application (09/372,319) that is relied upon for an earlier filing date under U.S.C. §120.

I hereby certify that this correspondence is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated below and is addressed to: Box Patent Application, Assistant Commissioner for Patents, Washington, D.C. 20231, on January 16, 2001.

Jan 16, 2001 T. Lester Wallace
Date T. Lester Wallace

Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office	Serial No.:
	Filing Date: January 16, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Para K. Segaram
(Use several sheets if necessary)	Group Art Unit:
"High Speed Communication System With A Feedback Synchronization Loop"	Examiner Name: Unknown
	Attorney Docket No. RA-162-1

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						

Foreign Patent Documents

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AF							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AG	"Draft Standard for Low-Voltage Differential Signals (LVDS) for Scalable Coherent Interface (SCI)", Draft 1.3, IEEE P1596.3-1995, pages i-viii, pages 1-34 (November 1995).
	AH	"Media Access Control (MAC) Parameters, Physical Layer, Repeater and Management Parameters for 1000 Mb/s Operation", IEEE Draft P802.3z/D5.0, pages i-vi, pages 01.1-01.8, 04.7-04.36, 05.1-05.6, 06.1-06.2, 22.1-22.10, 30.1-30.54, 30A.1-30A.44, 30B.1-30B.8, 31B.1-31B.2, 34.1-34.4, 35.1-35.30, 36.1-36.48, 36A.1-36A.4, 36B.1-36B.2, 37.1-37.26, 38.1-38.32, 39.1-39.20, 41.1-41.28, 42.1-42.8 (May 1998).
	AI	
	AJ	

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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